

1 A PLANAR VERTICAL CHANNEL DMOS STRUCTURE

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4 ABSTRACT

5 A DMOS power transistor has a vertical gate and a
6 planar top surface. A vertical gate fills a rectangular
7 groove lined with a dielectric material which extends
8 downward so that source and body regions lie on each side of
9 the dielectric groove. Carriers flow vertically between
10 source and body regions and the structure has a flat surface
11 for all masking steps.
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